

FIG. 1a

METHOD AND APPARATUS FOR AN ON-CHIP VARIABLE ACUITY IMAGER ARRAY
INCORPORATING ROLL, PITCH AND YAW ANGLE RATES MEASUREMENT

Inventors: Massie, Curzan, Baxter, Etienne-Cummings

Atty Docket: N001 100077

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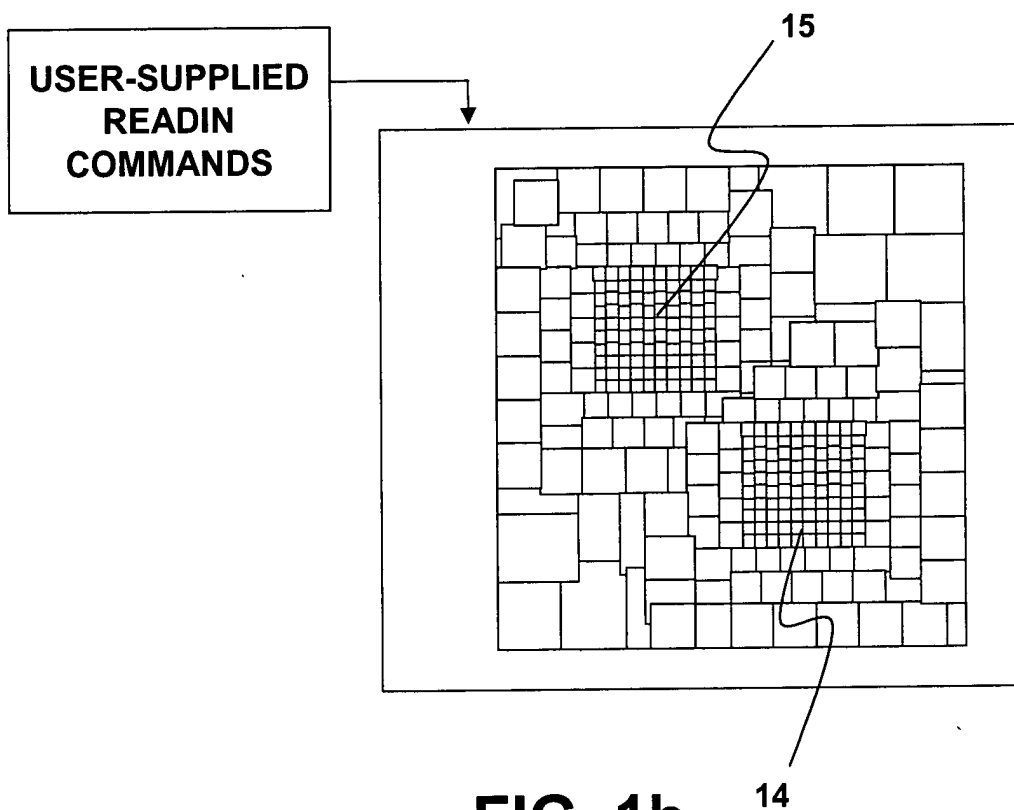


FIG. 1b

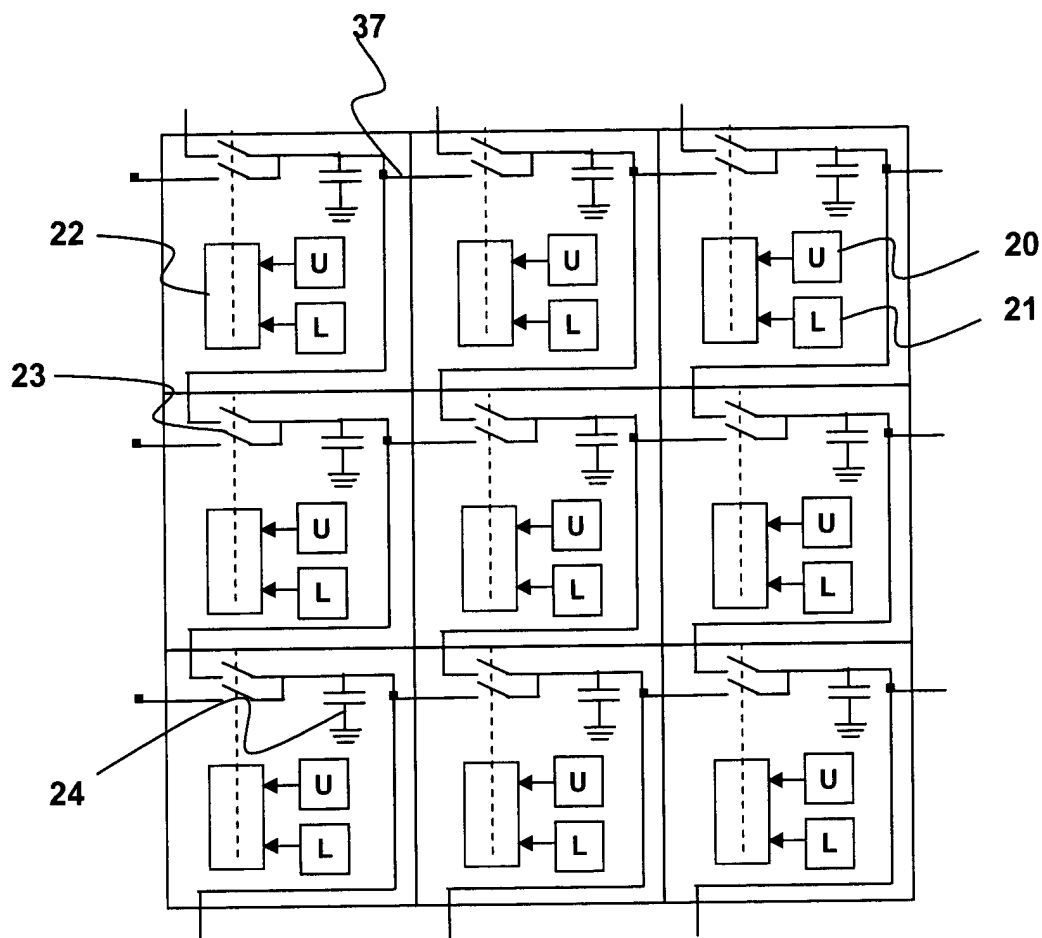


FIG. 2

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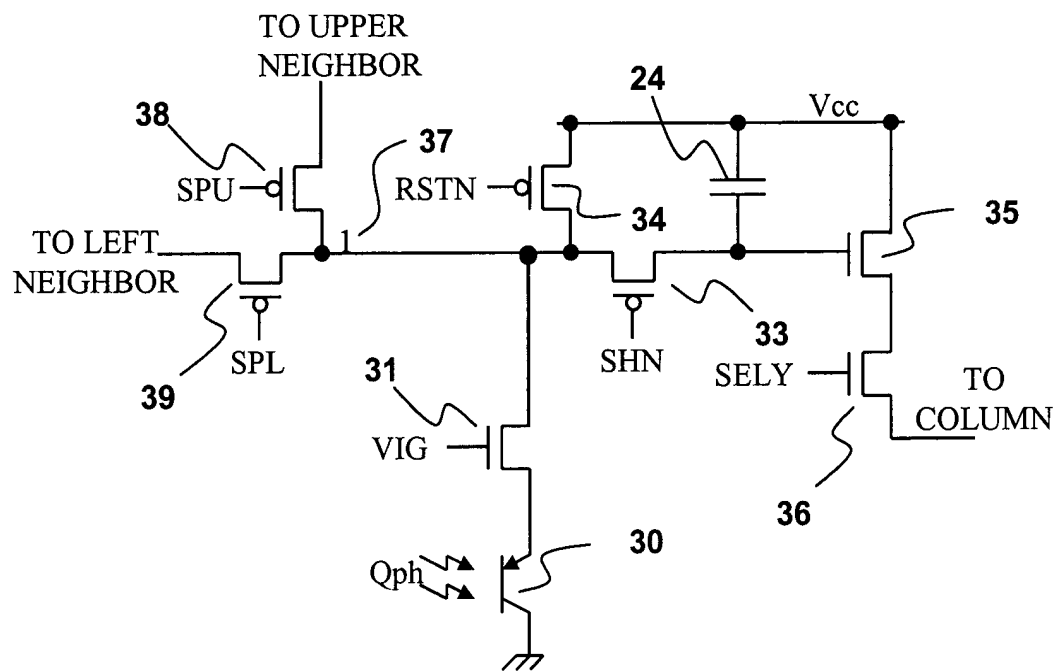


FIG. 3

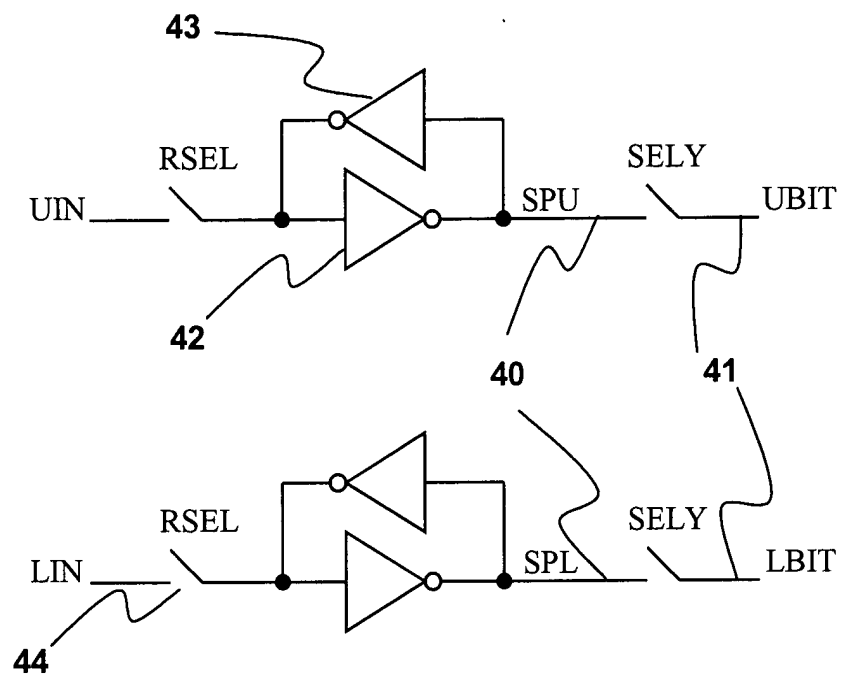


FIG. 4

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Figure 1 shows a 16x16 grid of 256 cells, each containing a 4-bit binary code (two digits). The grid is divided into four 8x8 quadrants. The top-left quadrant (rows 1-8, columns 1-8) contains the sequence 0001, 0011, 0101, 0111, 1001, 1011, 1101, 1111 in each row. The top-right quadrant (rows 1-8, columns 9-16) contains the sequence 0001, 0011, 0101, 0111, 1001, 1011, 1101, 1111 in each row. The bottom-left quadrant (rows 9-16, columns 1-8) contains the sequence 0001, 0011, 0101, 0111, 1001, 1011, 1101, 1111 in each row. The bottom-right quadrant (rows 9-16, columns 9-16) contains the sequence 0001, 0011, 0101, 0111, 1001, 1011, 1101, 1111 in each row. The grid is labeled with '50' on the left, '51' on the right, and '52' at the bottom.

FIG. 5

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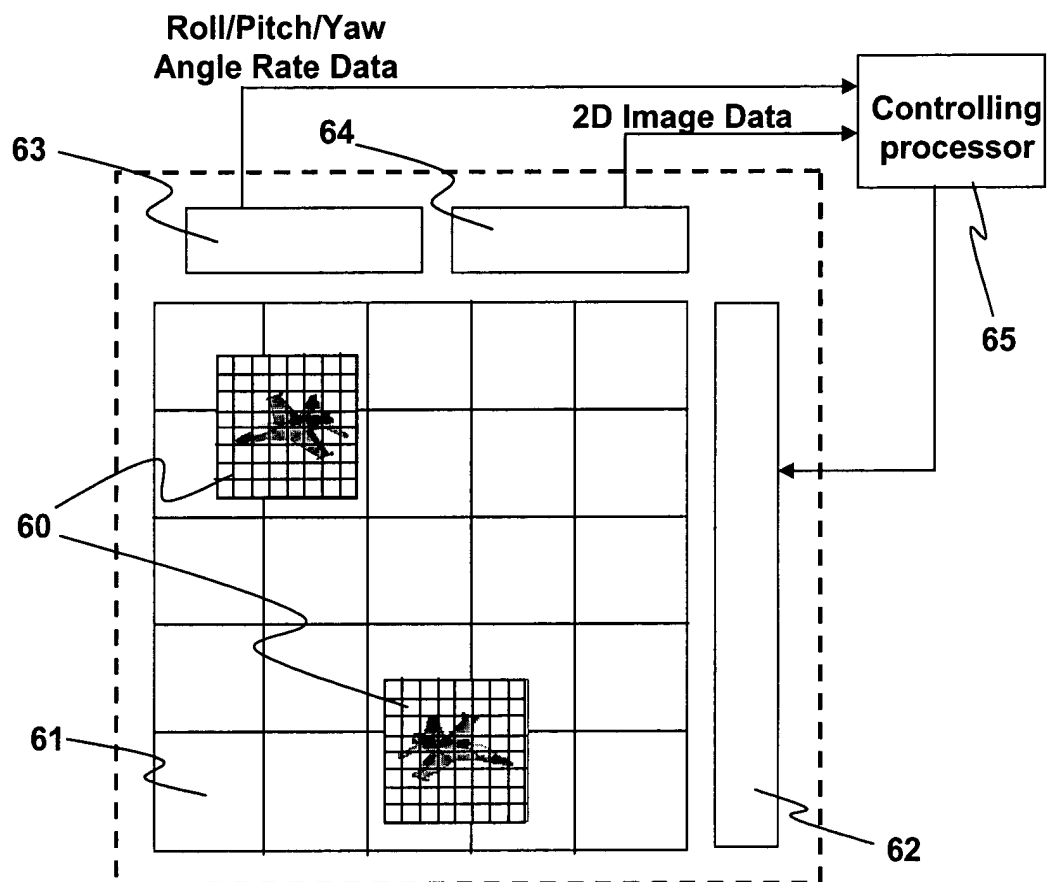


FIG. 6a

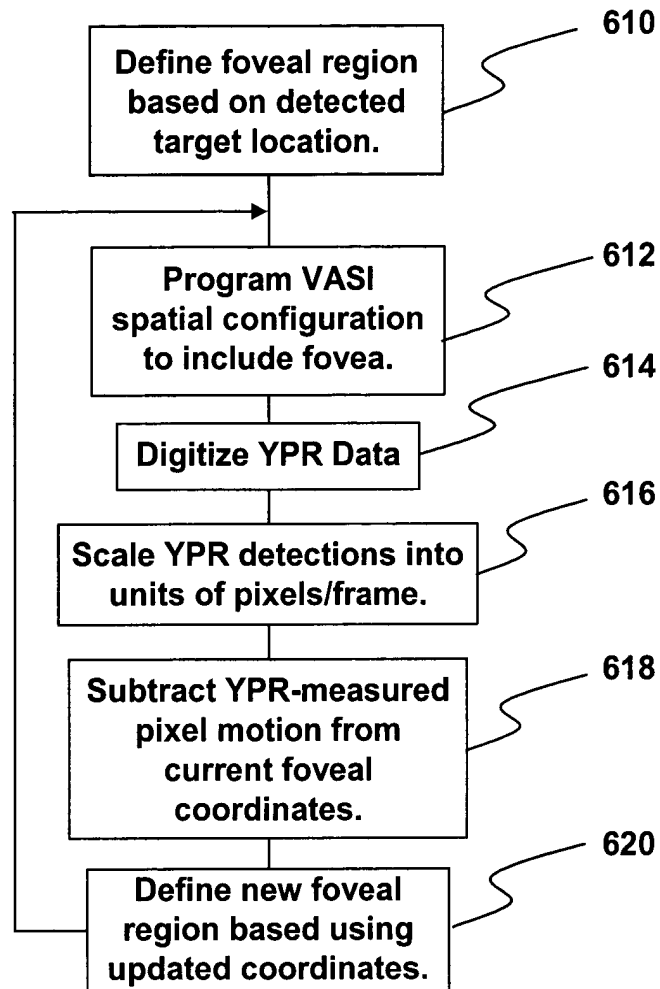


FIG. 6b

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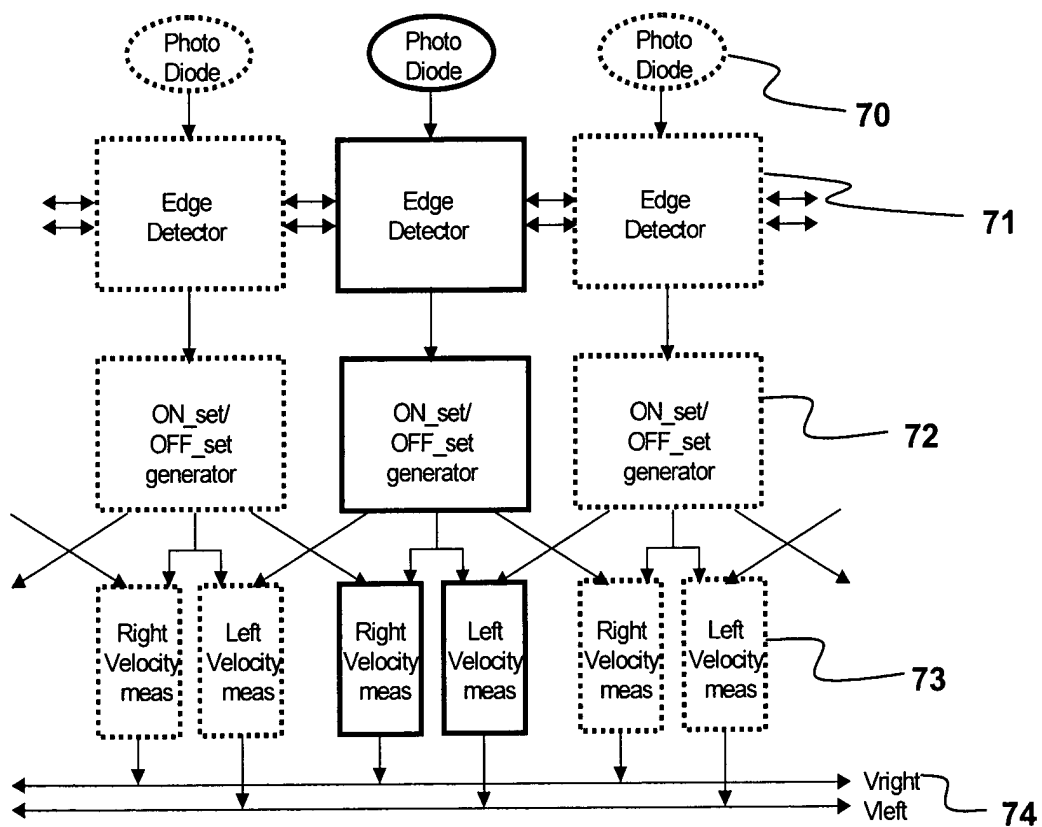


FIG. 7

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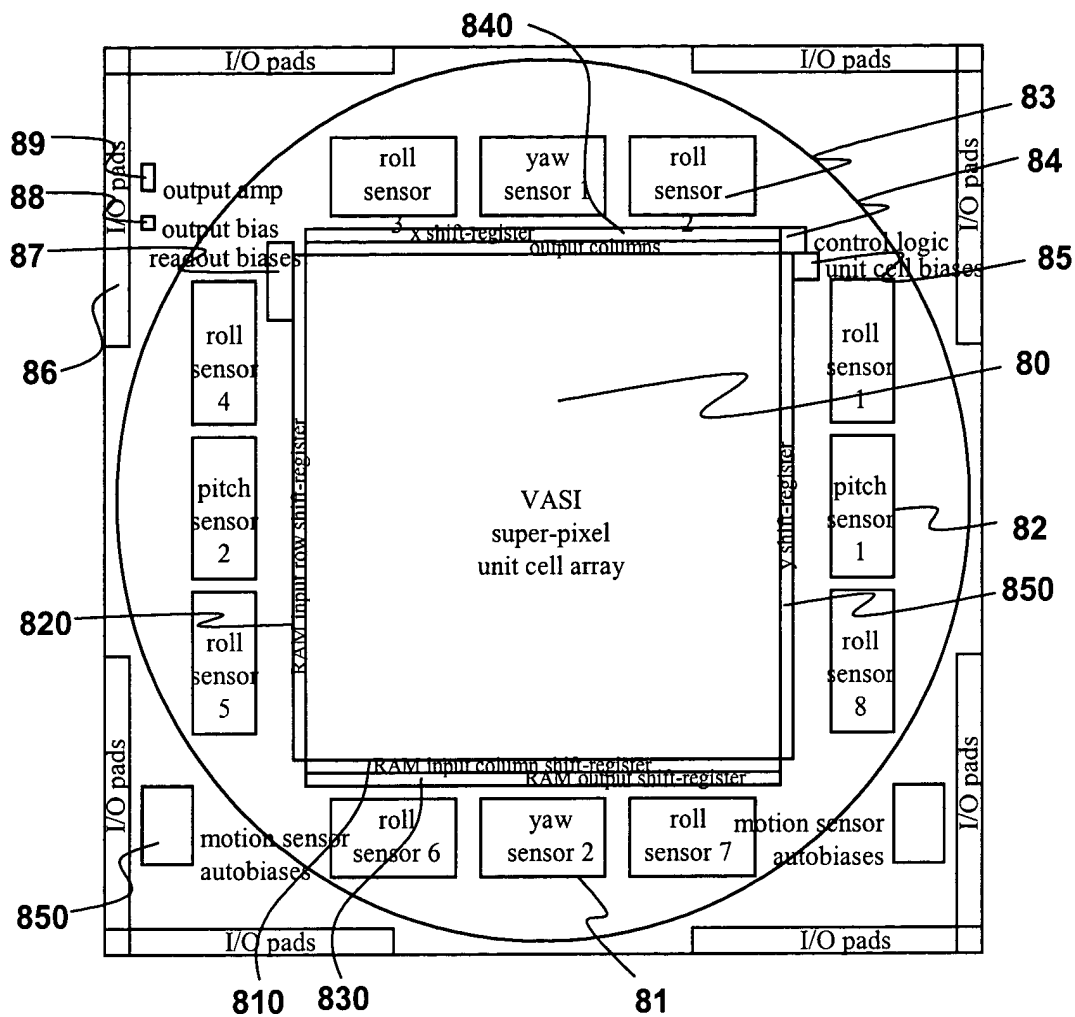


FIG. 8

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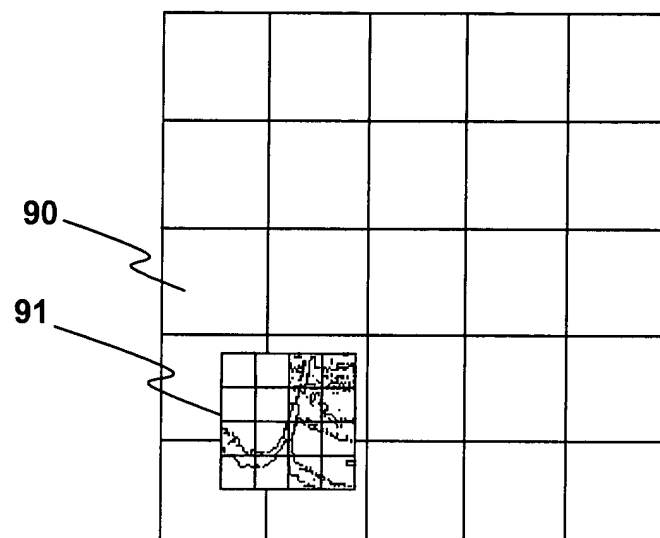


FIG. 9a

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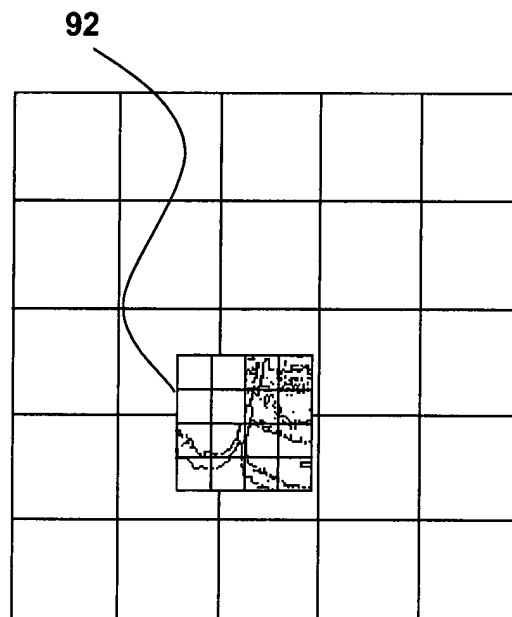


FIG. 9b

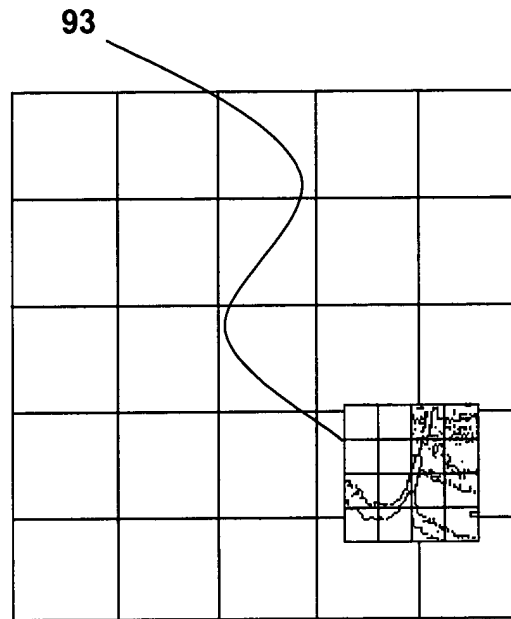


FIG. 9c